

FIG. 1

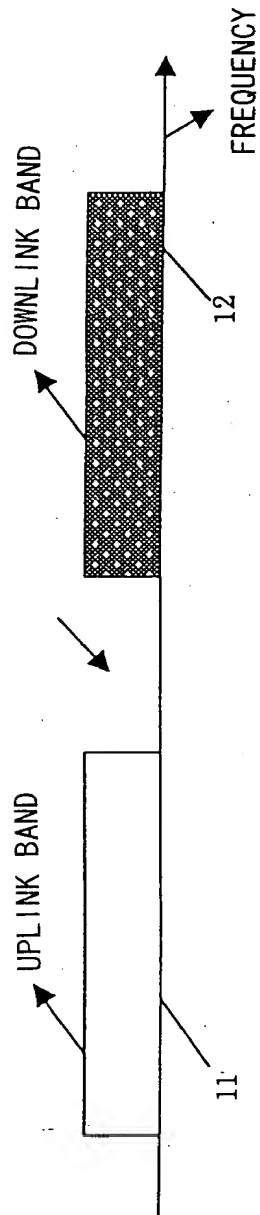


FIG. 2A

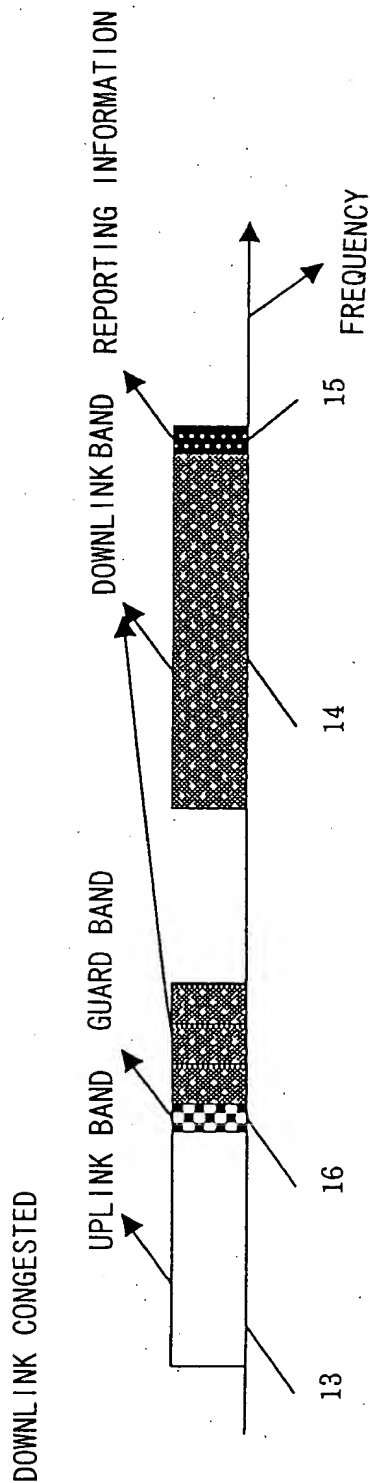


FIG. 2B

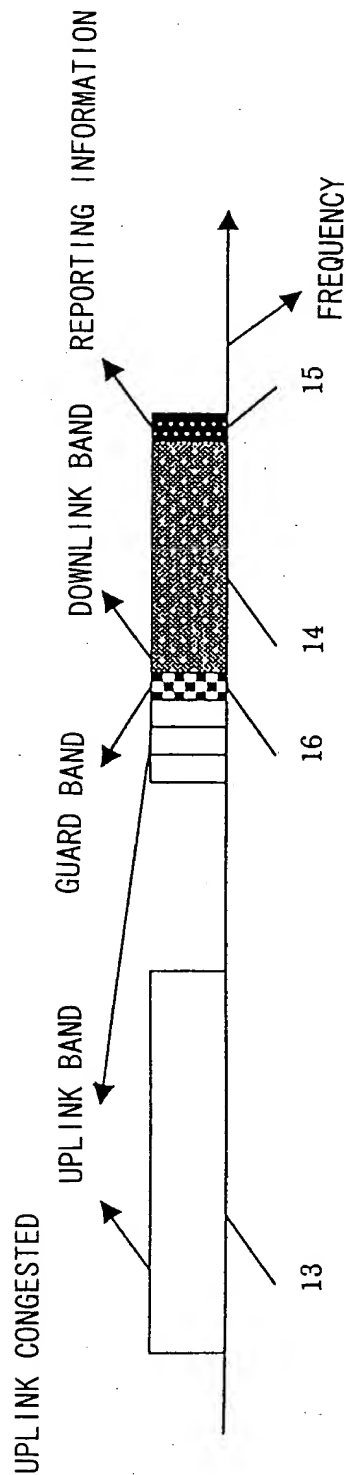
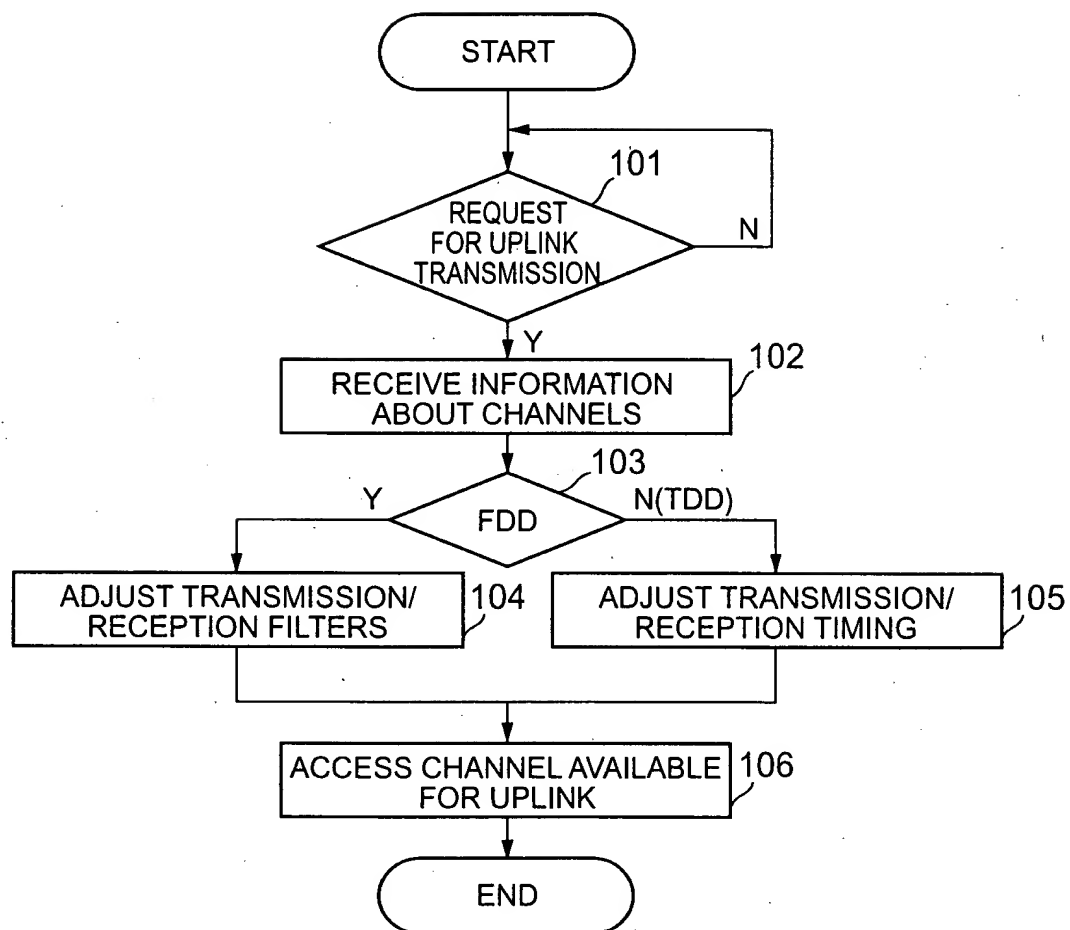
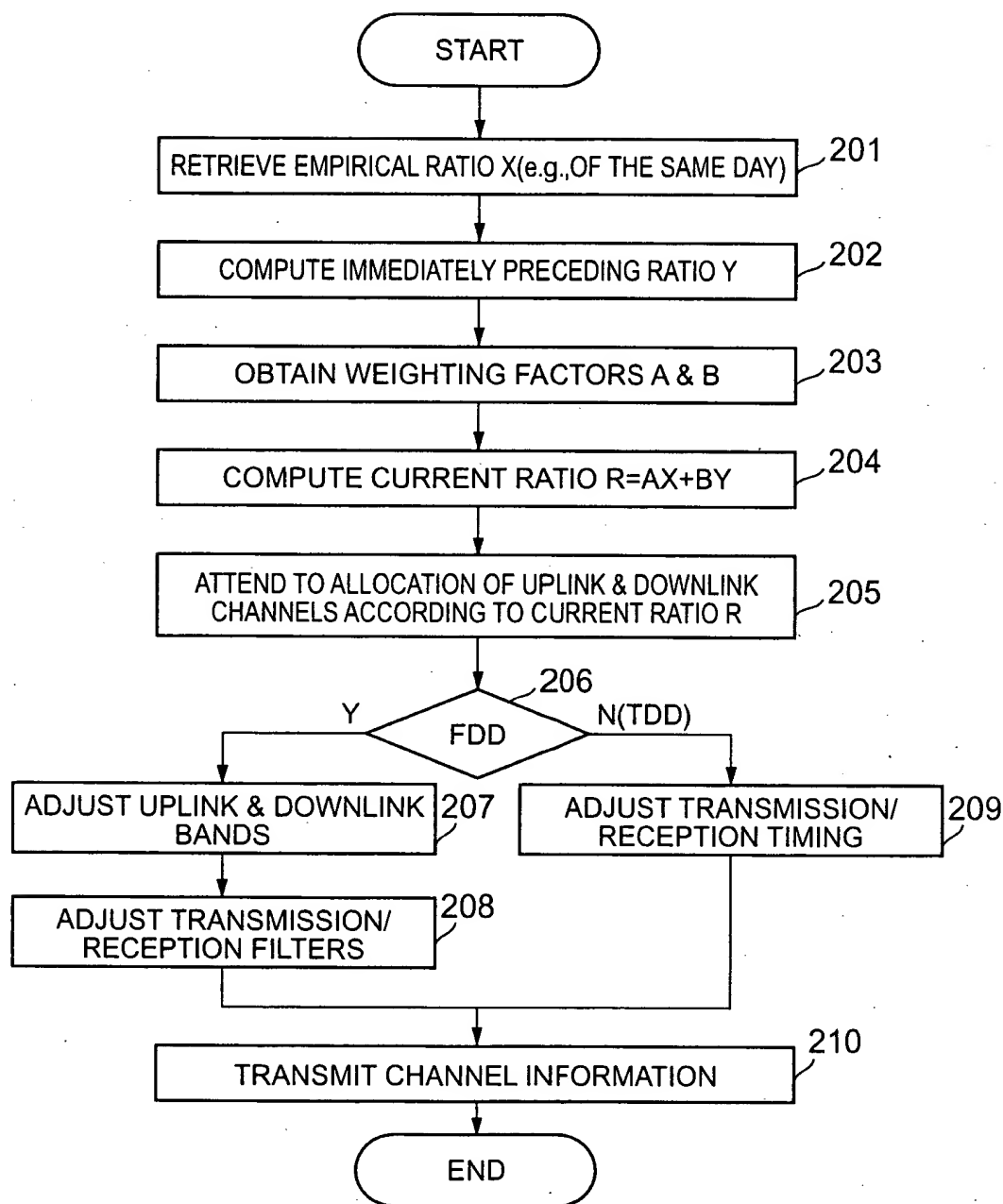


FIG.3



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FIG.4



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FIG. 5

	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY	SUNDAY
0:00~1:00	0.71	0.72	0.74	0.70	0.72	0.78	0.80
1:00~2:00	0.70	0.71	0.72	0.65	0.71	0.78	0.79
2:00~3:00	0.55	0.56	0.55	0.54	0.70	0.75	0.78
3:00~4:00	0.52	0.53	0.51	0.52	0.65	0.70	0.74
4:00~5:00	0.50	0.51	0.50	0.51	0.60	0.65	0.70
5:00~6:00	0.50	0.51	0.50	0.50	0.56	0.58	0.60
6:00~7:00	0.51	0.52	0.51	0.52	0.56	0.58	0.58
7:00~8:00	0.53	0.54	0.53	0.55	0.55	0.58	0.58
8:00~9:00	0.55	0.55	0.56	0.57	0.56	0.53	0.52
9:00~10:00	0.52	0.53	0.52	0.54	0.52	0.51	0.51
10:00~11:00	0.52	0.53	0.52	0.54	0.52	0.51	0.51
11:00~12:00	0.51	0.51	0.51	0.52	0.51	0.51	0.51
12:00~13:00	0.55	0.57	0.56	0.57	0.56	0.53	0.52
13:00~14:00	0.52	0.53	0.52	0.54	0.52	0.51	0.51
14:00~15:00	0.52	0.53	0.52	0.54	0.52	0.51	0.51
15:00~16:00	0.52	0.53	0.52	0.54	0.52	0.51	0.51
16:00~17:00	0.55	0.57	0.56	0.57	0.56	0.53	0.52
17:00~18:00	0.58	0.59	0.58	0.58	0.59	0.60	0.61
18:00~19:00	0.70	0.71	0.72	0.65	0.71	0.78	0.79
19:00~20:00	0.71	0.72	0.74	0.70	0.72	0.78	0.80
20:00~21:00	0.72	0.73	0.75	0.73	0.76	0.80	0.81
21:00~22:00	0.73	0.74	0.75	0.74	0.77	0.81	0.81
22:00~23:00	0.72	0.73	0.75	0.73	0.76	0.80	0.81
23:00~24:00	0.71	0.72	0.74	0.70	0.72	0.78	0.80

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FIG. 6

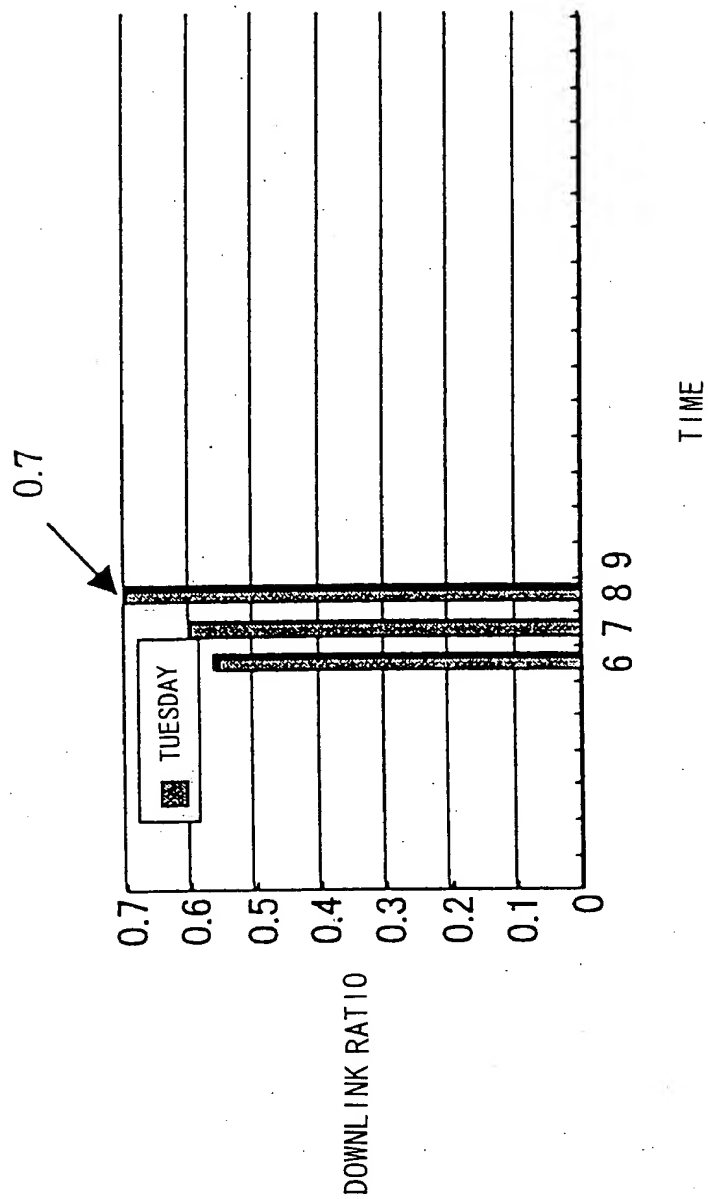
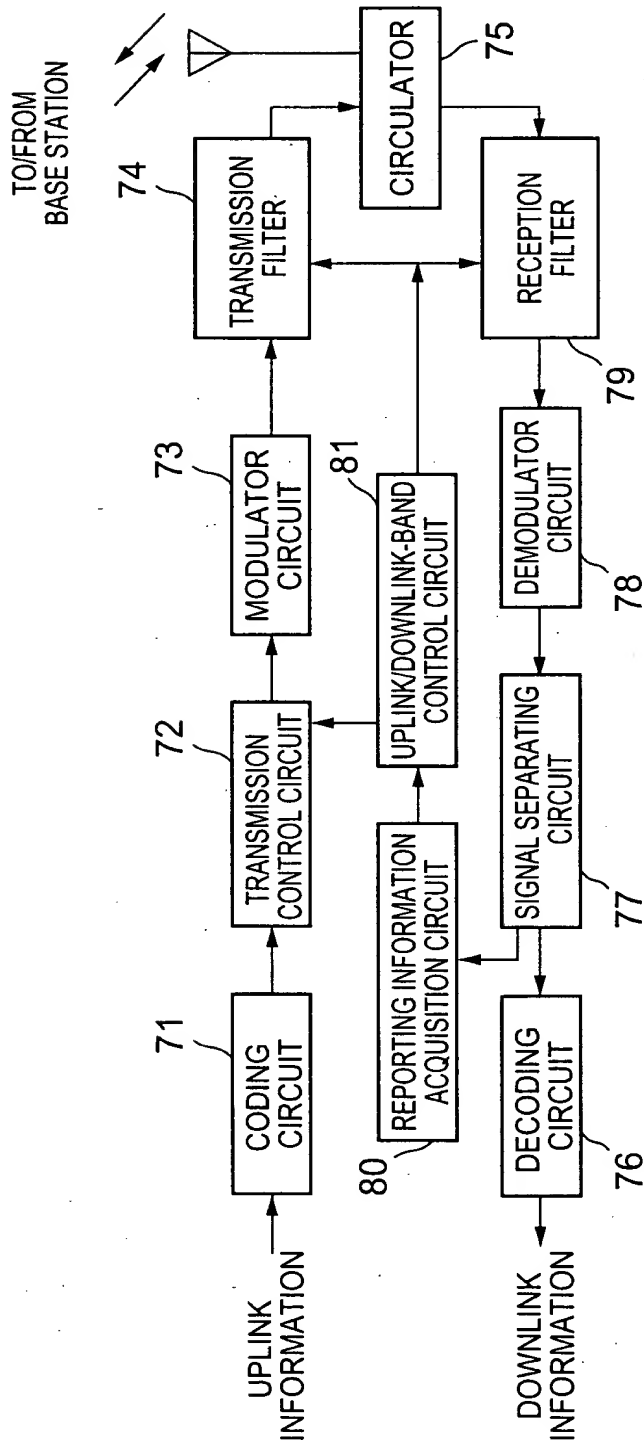


FIG.7



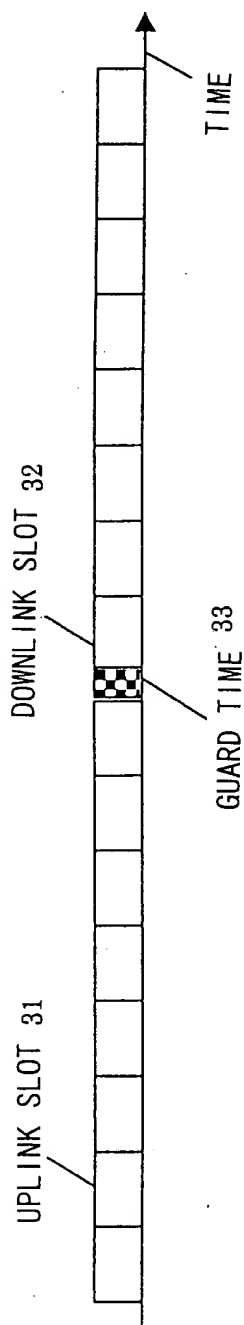
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The diagram illustrates the internal architecture of a mobile station. It features a circulator (90) that directs signals between an antenna and the internal circuitry. Uplink information enters via the antenna, passes through a reception filter (91), and is then processed by a demodulator circuit (92). The demodulator's output is split: one path goes to a decoding circuit (93) to produce the final uplink information, while the other path goes to an uplink traffic monitoring circuit (94). This monitoring circuit feeds into an uplink/downlink-ratio and uplink/downlink-channel-number computation circuit (95). This computation circuit also receives input from an empirical ratio memory (96) and provides control signals to an uplink/downlink-band control circuit (98) and a reporting control circuit (98). The band control circuit (98) manages the transmission path, which includes a modulator circuit (100) and a transmission filter (99). Downlink information is received through the antenna, passes through the transmission filter (99), and is then processed by a coding circuit (102) to produce the final downlink information. A downlink traffic monitoring circuit (103) also monitors the downlink path. The entire system is coordinated by a central reporting control circuit (98).



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FIG. 9



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FIG. 10A

DOWNLINK CONGESTED

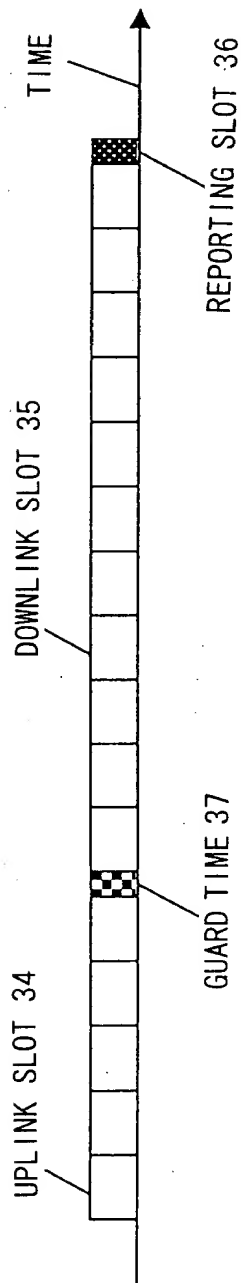


FIG. 10B

UPLINK CONGESTED

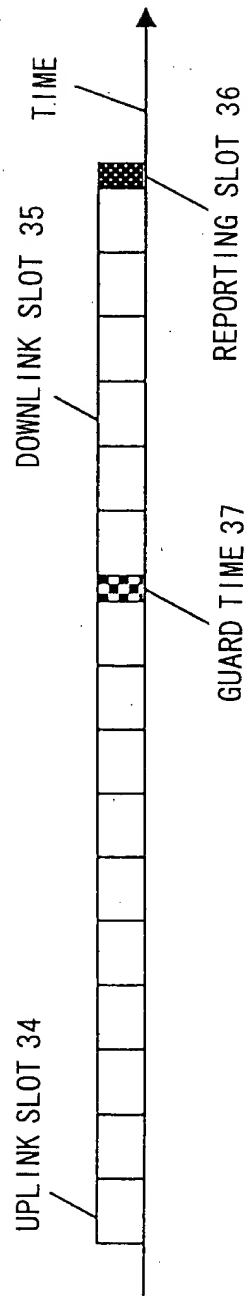
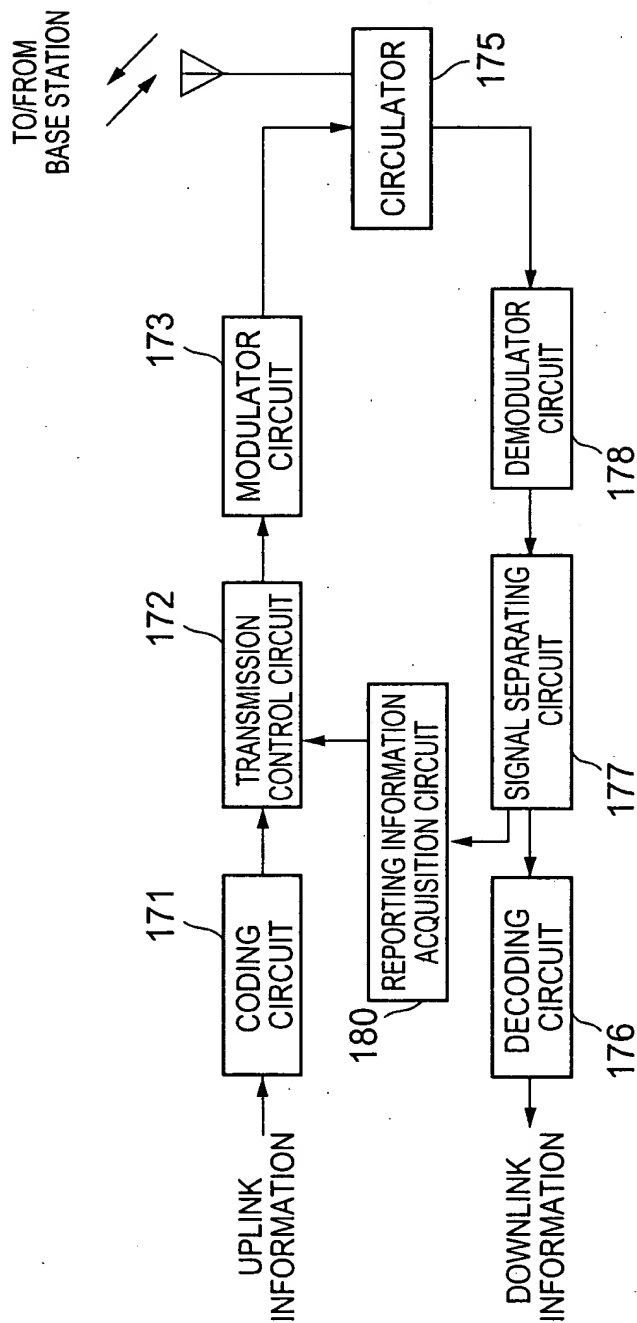


FIG.11



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